



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/726,166
	Filing Date	December 2, 2003
	First Named Inventor	Smith, Malcolm
	Group Art Unit	2817 2816
	Examiner Name	Unknown <i>Dinh Le</i>
Sheet 1 of 1	Attorney Docket No: 884.B66US1	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>DL</i>		SHU, KELIU, et al., "2.4-GHz monolithic fractional-N frequency synthesizer with robust phase-switching prescaler and loop capacitance multiplier", <u>IEEE Journal of Solid-State Circuits</u> , 38(6), (June 2003), 866-875	
<i>DL</i>		SHU, KELIU, et al., "A 16mW, 2.23-2.45GHz fully integrated Sigma Delta PLL with novel prescaler and loop filter in 0.35 mu m CMOS", <u>2003 IEEE Radio Frequency Integrated Circuits (RFIC) Symposium. Digest of Papers</u> , Philadelphia, Pennsylvania, June 8-10, 2003, (June 8, 2003), 181-184	

EXAMINER	<i>[Signature]</i>	DATE CONSIDERED	<i>6/3/05</i>
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